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(54) NOISE RIDING THRESHOLD CONTROL WITH IMMUNITY TO SIGNALS WITH HIGH PULSE REPETITION FREQUENCIES AND HIGH DUTY CYCLES

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(56) References Cited

U.S. PATENT DOCUMENTS

3,603,996 A	• 9/1971	Murchison et al 342/28
3,838,422 A	• 9/1974	MacArthur et al 342/93
3,946,382 A	* 3/1976	Kossiakoff et al 342/93
4,001,826 A	• 1/1977	Moulton 342/110
4,051,473 A	• 9/1977	Hooker, Jr 342/93
4,062,012 A	• 12/1977	Colbert et al 342/90
4,122,448 A	* 10/1978	Martin 342/174
4,642,641 A	• 2/1987	Campbell 342/88
5,708,437 A	* 1/1998	Gellekink 342/91

OTHER PUBLICATIONS

"Maximizing noise-limited detection performance in medium PFR radars by optimizing PFR visibility", Moorman, R.A.; Westerkamp, J.J., Aerospace and Electronics Conference, 1993. NAECON 1993, Proceedings of the IEEE 1993 National, 199 pp. 288-293 vol.*

"Track-before-detect performance for a high PRF search

"Track-before-detect performance for a high PRF search mode", Harmon, J.L., Radar Conference, 1991., Proceedings of the 1991 IEEE National, 1991, pp. 11-15.*

"Cascaded detector for multiple high-PRF pulse Doppler radars", Gerlach, K.; Andrews, G.A., Aerospace and Electronic Systems, IEEE Transactions on, vol. 26, Issue: 5, Sep. 1990, pp. 754-767.*

* cited by examiner

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(57) ABSTRACT

A system and method is disclosed for a radar receiver, such as a wideband crystal video early warning receiver, to automatically detect the noise level of the radar receiver with immunity to high pulse repetition frequencies and high duty cycle signals. The noise riding threshold circuit utilizes high frequency components of the noise and, to the attenuated extent present, high frequency components of the video signal to produce the noise riding threshold voltage. An amplifier gain control permits adjusting the noise-riding threshold to a fixed relative level. In a preferred embodiment, the noise riding threshold control of the present invention utilizes current feedback amplifiers for wide bandwidth, high gain video amplifiers.

18 Claims, 1 Drawing Sheet

